

Substation Automation and Protection Division



## Using the DPU2000R Independent Breaker Failure Function

In the DPU2000R feeder protection system, an internal Breaker Failure function is enabled when the breaker is in a closed position, triggered by the assertion of the master trip signal and operated per its settings in the Configuration settings group. The master trip signal is driven by the overcurrent elements mapped to it as seen with the DPU2000R interface software WinECP. Where breaker failure detection is also desired with operation of overcurrent functions not mapped to the master trip, non-overcurrent functions such a voltage and frequency within the DPU2000R, an auxiliary relay external to the DPU2000R or for a second breaker(see Application Note AN-84D-01 for multiple breaker operations), the DPU2000R's Independent Breaker Failure function can be utilized.

The Independent Breaker function has two features: 1) breaker failure timer(BFT) and 2) a Re-trip feature. The two features comprise of the same three parts: a) an enabler, b) a trigger and c) pickup and dropout timers. The "enabler" enables the feature and is provided externally through the logical input *EXTBFI* or internally through the measured phase or neutral current levels. The "trigger" starts the breaker failure pickup timer, the time within which the breaker should operate, and the Re-Trip pickup timer, the time delay before issuing a second trip signal to operate a second trip circuit for the same breaker or the trip circuit of a backup breaker. The trigger is programmed using the logical input *BFI* and examples would be a non-master trip protection function operation or external relay operation. The BFT dropout timer defines the time independent breaker failure logical output *BFT* will remain energized, after a breaker failure condition occurs, and the Re-Trip dropout time defines the time the Re-trip signal will remain energized after the Re-trip timer expires. Note that the flexibility of this function allows it to be used as a breaker failure to close element without using the Re-Trip portion.

Figure 1 shows the logic diagram for this function while Figures 2 and 3 show the programmable mapping for implementing it. The programmable mapping utilizes User Logical and Feedback I/O. For more information on these logicals, see Application Note AN-88D-01 and Section 6 of the DPU2000R Instruction Booklet.



Figure 1 - Breaker Failure Logic

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Figure 2 - Programmable Input mapping: (a) physical inputs; (b) feedback inputs.

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rammable Outp								
TIMERS:				0.00		0.00		
NAME	Out-1	Out-2 BKRFAIL	Out-3	Out-4	Out-5	Out-6		
NAME:	JULUSE	BNRFAIL	RE-TRIP					
LOGIC	OR	OR	OR	OR	OR	OR		
•								
ISE	-							
		-						
)			-					
P (U<)								
3P (3U<)								
U>)								
S-1 (f<1)								
i-2 (f<2)								
R-1 (b1)							,	~ (
R-2 (f>2)							•	
0-1 (f>fs1)								
0-2 (f>fs2)								
rammable Outpu	ute - Feedba	cke						
anniable o'ach		GKS						
	FB01	FB02	FB03	FB04	FB05	FB06	FB07	FB08
LOGIC	OR	OR	OR	OR	OR	OR	OR	OR
		100						1000
					P			
OSE					E.			
RIP LOSE								
.OSE T eTrp								
.OSE T :Trp :TP (U<)					S. C.			
.OSE :T :Trp :1P (U<) :3P (3U<)					No No			
.OSE :T :T P (U<) :3P (3U<) I(U>)								
OSE FT #Trp *1P (U<) *3P (3U<) (U>) S-1 (f<1)								
OSE T FTrp -1P (U<) -3P (3U<) (U>) S-1 (f<1) S-2 (f<2)								
OSE T •Trp •1P (U<) •3P (3U<) (U>) S-1 (f<1)								
DSE T Trp 1P (U<) 3P (3U<) (U>) 5-1 (K<1) 5-2 (K<2) 3-1 (K))								

Figure 3 - Programmable Output Mappings: (a) physical outputs; (b) feedback outputs.

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