



INSTALLATION • OPERATION • MAINTENANCE I N S T R U C T I O N S

TYPE SGR-52 RECLOSING RELAY

APPLICATION

The SGR-52 Reclosing Relay provides for adjustable time delay reclosure of an electrically-operated circuit breaker, and automatically resets itself if the breaker remains closed for a predetermined adjustable time interval. If the breaker retrijs before the end of the interval, the resetting operation of the relay is interrupted until the breaker is manually closed. Thus, the reclosing relay is applicable to either attended or non-attended stations.

CONSTRUCTION AND OPERATION

- * The SGR-52 is a static relay consisting of a Reclose circuit board, a Reset circuit board, and an Indicator circuit board. The Reset circuit board contains (1) a timing circuit (2) a flip-flop control circuit, (3) a close relay circuit, and (4) a flip-flop set circuit. The Reclose board contains a reclose time delay circuit with permit and block inputs. The Indicator board provides amplification for the lockout indicator. All components except the dropping resistor, lockout indicator, and the close relay are mounted on a printed circuit board. All components are identified on the internal schematic in Figure 3.

Timing Circuit

The timing circuit is a unijunction relaxation oscillator consisting of unijunction transistor Q3, capacitors C2 and C13 and resistors R10 and R62. After a preset time interval controlled by the adjustable time dial potentiometer R62, the relaxation oscillator fires and feeds an output pulse to the flip-flop control circuit.

Flip-Flop Control Circuit

The flip-flop control circuit consists of transistors Q4 and Q5 and resistors R12 to R21. The flip-flop circuit resets when pulsed by the timing circuit and thereby activates the close relay circuit by turning transistor Q7 off.

Close Relay Circuit

The close relay circuit consists of transistors Q7, Q8 and Q9, resistors R24 to R30 and the close relay. The turn-off of transistor Q7 by the flip-flop control circuit switches transistors Q8 and Q9 to the on state to activate the close relay.

Flip-Flop Set Circuit

The flip-flop set circuit consisting of transistors Q6 and Q1, resistors R22 and R23, and capacitor C5, sets the flip-flop control circuit and the reclose time delay circuit when the breaker closes, opening the 52b contact. Transistors Q1 and Q6 switch to the on state and discharge capacitor C5 through Q6 to turn transistor Q5 off and transistor Q15 on to set the flip-flop and the reclose time delay circuits simultaneously.

Lockout Indicator Circuit

The lockout indicator circuit is controlled by the state of the flip-flop control circuit. Consisting of amplifier transistors Q10 and Q11, the amber light is lit when the flip-flop is in the set state, and the relay is locked out if the breaker is open at this time.

Reclose Time Delay Circuit

The reclose time delay circuit consists of a flip-flop arrangement of transistors Q12 and Q13, capacitor C11 which charges through the time dial setting potentiometer R63, zener diode Z10, and transistor Q16 and Q17.

Theory of Operation

The following description is made with reference to Figure 3.

Let us assume that the breaker is open and normal voltage is applied to the relay. Under these conditions, transistors Q2, Q4, Q7, Q11, Q13 and Q17 are on and the amber lockout indicator is ener-

gized. When the breaker is closed, the 52b contact opens and removes the shorting of the base drive to transistor Q1 turning it on. The turn on of Q1 shorts the base drive to Q2 turning it off, which causes diode D3 to be reverse biased. This removes the short-circuit from capacitors C2 and C13 allowing them to charge through R10 and potentiometer R62 to the firing voltage of unijunction transistor Q3. The time required for C2 and C13 to charge and fire Q3 is controlled by potentiometer R62 set to a calibrated time dial. When Q3 fires, C2 and C13 discharge through Q3 and R12 to cause a voltage rise across R12. This causes the voltage on the emitter of Q4 to rise above its base voltage, turning it off and flip-flop transistor Q5 on. This is the reset state of the flip-flop. The turn off of transistor Q4 turns Q10 on and Q11 off to de-energize the lockout indicator. When flip-flop transistor Q5 turns on, its collector voltage drops to a low level and removes the base drive to Q7 turning it off. The low collector voltage of Q5 also disables the timing circuit by forward biasing diode D4 and providing a path for current to flow through Q5 so that capacitors C2 and C13 cannot charge up to a point where it will again fire unijunction Q3. When the turn on of transistor Q5 turns transistor Q7 off, the on transistor Q17 provides a shorting path for the rise in potential of the collector of Q7 that would normally supply the base drive for transistor Q8 to turn on and activate the rest of the close relay circuit.

If a fault appears on the protected line and a protective relay opens the breaker, the 52b contacts make up. If at this time a positive going "Permit" pulse is applied to the reclose time delay circuit, the breaker will close after the preset time delay has elapsed. This is accomplished by the positive going pulse turning normally off transistor Q12 on, placing the base of Q13 to ground potential, turning it off, and thereby removing the short from capacitor C11 allowing it to charge through potentiometer R63. Time variation is controlled by the dial setting of R63. The voltage level on capacitor C11 reaches a point where zener diode Z10 breaks down and allows base current to flow into Q16. This causes Q16 to go from the normally off to the normally on state, depriving Q17 of base drive, turning it off. This removes the short from the base of Q8 allowing it to turn on. The turn on of Q8 allows base current to flow from Q9 turning it on. The switching on of Q9 energizes the close relay, closing the normally open contact to the positive battery supply. This provides a path from battery positive, through the close relay contact, to energize and immediately reclose the breaker.

The reclosing of the breaker reopens the 52b contact, switching transistors Q1 and Q6 on. The flip-flop control circuit and the reclose time delay circuit are then set by capacitor C5 discharging through Q6 and R19 to turn transistor Q5 off, and through Q6 and R55 and R50 to turn transistors Q15, Q14 and Q13 on. With Q5 turned off, Q7 is supplied with base drive switching it on, and Q8 and Q9 off. With transistor Q9 turned off, the close relay is de-energized and its contact reopens. With the turn off of Q5 and the setting of the control flip-flop, Q4 is turned on and the lockout indicator circuit is energized by Q4 shorting out the base drive to Q10, turning it off and switching Q11 on to energize the amber lockout indicator.

When the 52b contacts reopened and switched transistor Q1 on, the base drive to Q2 was shorted, and Q2 turned off to reverse bias diode D3 and allow capacitors C2 and C13 to again charge through R62 and R10. Let us assume that a protective relay operated to trip the breaker before capacitors C2 and C13 have charged to the firing level of Q3. When the breaker opens, the 52b contact closes, switching Q1 off and Q2 on, forward biasing diode D3. This short circuits capacitors C2 and C13 had not reached a level to fire Q3, the control flip-flop has not changed state, the close relay circuit remains off, and the lockout indicator remains on. The breaker will remain locked out until manually closed.

If at any time during the reclosing cycle a signal is applied to the block input of the reclose board, the relay will not reclose until the block signal is removed and a permit signal is applied to activate the reclose circuitry.

Reclose initiation can only take place when the breaker is open, since the circuitry associated with transistor Q1 shorts the "Permit" signal through diode D2 to ground, allowing the "Permit" signal to turn transistor Q12 on only when Q1 is in the off state (52b contact closed - Breaker open).

CHARACTERISTICS

Voltage Rating

The SGR-52 is rated for 48 or 125 volts d-c. Unless otherwise specified, the relays are connected for 125-volt operation when shipped.

Temperature Range

- * The SGR-52 is designed to operate over a temperature range from -20°C to $+55^{\circ}\text{C}$ with timing variations of not more than $\pm 55^{\circ}\text{C}$.

Energy Requirements

55 milliamperes at rated voltage.

SETTINGS**Reset Time Setting**

The reset time is controlled by front-mounted potentiometer R62 which has a calibrated time dial. The reset time is variable from 3 to 30 seconds.

Reclose Time Setting

The reclose time is controlled by front-mounted potentiometer R63 which has a calibrated time dial. The reclose time is variable from 0 to 2 seconds or 2 to 20 seconds depending on the style of the relay.

ADJUSTMENTS AND MAINTENANCE

The proper adjustments to insure correct operation of this relay have been made at the factory, and no further adjustment should be required.

Acceptance Test

The following check is recommended to insure that the relay is in proper working order. All checks can best be performed by connecting the SGR-52 as shown in Figure 5.

Place the cal-operate switch in the operate position. Push PB-1 and wait until the time set on the reset timer has elapsed and the lockout indicator turns off. Push PB-2 to trip relay B. Push the Permit switch and the B relay should close after the time set on the reclose timer has elapsed. After relay B closes, if PB-2 is pushed to again trip relay B within the reset time setting (before the lockout indicator turns off), the B relay should trip and remain locked out.

Calibration Check

The following procedures may be used to accurately check the time dial calibrations. Using Figure 5, the tester can accurately check the calibrations by using a timer as shown.

(1) Reclose Time Delay

With the cal-operate switch in the cal position, apply rated voltage. Push PB-1 and wait for the lockout indicator to turn off. Push PB-2 to trip the relay. Push the Permit switch to start the timer. The CR contacts should close to stop the timer after the time set on the reclose timer dial has elapsed.

(2) Reset Time Delay

Disconnect the lead to reclose board terminal B. Apply rated voltage. Push PB-1 to start the timer. The CR contacts should close to stop the timer after the time set on the reset timer time dial has elapsed.

Routine Maintenance

All relays should be checked at least once every year or at such other intervals as may be dictated by experience to be suitable to the particular application.

Trouble Shooting

Use the following procedure to locate the source of trouble in the event of improper relay operation.

- (1) Inspect all wires and connections.
- (2) Check resistances as listed in the Electrical Parts List.
- (3) Check voltages or waveforms as listed under Electrical Checkpoints using a vacuum tube voltmeter and/or an oscilloscope.

Electrical Checkpoints

Apply rated voltage through a switch to relay terminals 8 and 9. Terminal 9 is positive.

Set the reset time dial for 15 seconds, and the reclose time dial for 2 seconds.

Apply rated voltage to the relay to test the circuit boards.

Apply voltage before each testpoint check and interrupt it after each check. Take test point readings before and after the reset time shown on the time dial.

Use the following table to determine the correct voltages or waveforms at the indicated point. Refer to Figures 6, 7 and 8 for circuit board component layouts.

RENEWAL PARTS

Repair work can be done most satisfactorily at the factory. However, interchangeable parts can be furnished to the customers who are equipped for doing repair work. When ordering parts, always give the complete nameplate data.

TYPE SGR-52 RECLOSING RELAY

CIRCUIT SYMBOL	DESCRIPTION	WESTINGHOUSE STYLE NUMBER	CIRCUIT SYMBOL	DESCRIPTION	WESTINGHOUSE STYLE NUMBER
R E S I S T O R S					
R1	82,000 ½ W 5%	184A763H73	R34	20,000 ½ W 5%	184A763H58
R2	10,000 ½ W 5%	184A763H51	R35	2,000 ½ W 5%	184A763H34
R3	4,700 ½ W 5%	184A763H43	R36	47,000 ½ W 2%	629A531H72
R4	4,700 ½ W 5%	184A763H43	R37	4,700 ½ W 2%	629A531H48
R5	20,000 ½ W 5%	184A763H58	R38	82,000 ½ W 2%	629A531H78
R6	3,900 ½ W 5%	184A765H41	R39	27,000 ½ W 5%	184A763H61
R7	10,000 ½ W 5%	184A763H51	R40	33,000 ½ W 5%	184A763H63
R8	22,000 ½ W 5%	184A763H59	R41	27,000 ½ W 5%	184A763H61
R9	33,000 ½ W 5%	184A763H63	R42	33,000 ½ W 5%	184A763H63
R10	82,500 ½ W 2%	836A503H70	R43	15,000 ½ W 5%	184A763H55
R11	680 ½ W 5%	184A763H23	R44	15,000 ½ W 5%	184A763H55
R12	47 ½ W 5%	187A290H17	R45	82,000 ½ W 5%	184A763H73
R13	330 ½ W 5%	184A763H15	R46	820 ½ W 5%	184A763H25
R14	10,000 ½ W 5%	184A763H51	R47	10,000 ½ W 5%	184A763H51
R15	10,000 ½ W 5%	184A763H51	R48	10,000 ½ W 5%	184A763H51
R16	33,000 ½ W 5%	184A763H63	R49	4,700 ½ W 5%	184A763H43
R17	10,000 ½ W 5%	184A763H51	R50	10,000 ½ W 5%	184A763H51
R18	33,000 ½ W 5%	184A763H63	R51	47,000 ½ W 2%	629A531H72
R19	47 ½ W 5%	187A290H17	R52	82,000 ½ W 2%	629A531H78
R20	330 ½ W 5%	184A763H15	R53	2,000 (0-2 sec.) 1%	836A503H33
R21	10,000 ½ W 5%	184A763H51	R53	18,200 (2-20 sec.) 1%	836A503H55
R22	2,000 ½ W 5%	184A763H34	R54	4,700 ½ W 2%	629A531H48
R23	30,000 ½ W 5%	184A763H62	R55	1,000 ½ W 5%	184A763H27
R24	10,000 ½ W 5%	184A763H51	R56	1D051 20K Thermistor	185A211H05
R25	180,000 ½ W 5%	184A763H81	R57	15,000 ½ W 5%	184A763H55
R26	68,000 ½ W 5%	184A763H71	R58	33,000 ½ W 5%	184A763H63
R27	10,000 ½ W 5%	184A763H51	R59	10,000 ½ W 5%	184A763H51
R28	33,000 ½ W 5%	184A763H63	R60	33,000 ½ W 5%	184A763H63
R29	10,000 ½ W 5%	184A763H51	R61	33,000 ½ W 5%	184A763H63
R30	10,000 ½ W 5%	184A763H51	R62	1 meg. ohm,	185A086H23
R31	20,000 ½ W 5%	184A763H58	R63	250,000	185A086H11
R32	180,000 ½ W 5%	184A763H81	R64	1900 tapped at 510,	11D9511H10
R33	10,000 ½ W 5%	184A763H51		25 W 5%	

CIRCUIT SYMBOL	DESCRIPTION	WESTINGHOUSE STYLE NUMBER	CIRCUIT SYMBOL	DESCRIPTION	WESTINGHOUSE STYLE NUMBER
* CAPACITORS			* ZENER DIODES		
C1	.047 mfd, 200V, 5%	849A437H04	Z1	1N3049B	187A936H13
C2	22 mfd, 100V, 5%	862A177H04	Z2	1R200	629A369H01
C3	1.5 mfd, 35V, 10%	187A508H09	Z3	1N758	186A797H01
C4	.01 mfd, 200 V, 10%	764A278H10	Z4	HW20B	185A212H14
C5	4.7 mfd, 35 V, 20%	184A661H12	Z5	1N3686B	185A212H06
C6	.047 mfd, 200 V, 5%	849A437H04	Z6	1N957B	186A797H06
C7	.01 mfd, 200 V, 10%	764A278H10	Z7	1N758	186A797H01
C8	.47 mfd, 35 V, 20%	187A508H05	Z8	1N957B	186A797H06
C9	1.5 mfd, 35 V, 10%	187A508H09	Z9	1N3686B	185A212H06
C10	.047 mfd, 200 V, 5%	849A437H04	Z10	(0-2 sec.) 1N957B	186A797H06
C11	22 mfd, (0-2 sec.) 100 V, 5%	862A177H04	Z10	(2-20 sec.) 1N960B	186A797H10
C11	68 mfd, (2-20 sec.) 60 V, 5%	862A177H03	Z12	1R200	629A369H01
C12	68 mfd, (2-20 sec.) 60 V, 5%	862A177H03	DIODES		
C13	1.5 mfd, 35 V, 5%	187A508H18	D1	T1-55	183A790H09
TRANSISTORS			D2	T1-55	183A790H09
Q1	2N3417	848A851H02	D3	T1-55	183A790H09
Q2	2N3417	858A851H02	D4	T1-55	183A790H09
Q3	4JX5E695	629A435H02	D5	T1-55	183A790H09
Q4	2N3417	848A851H02	D6	T1-55	183A790H09
Q5	2N3417	848A851H02	D7	T1-55	183A790H09
Q6	2N3645	848A851H01	D8	T1-55	183A790H09
Q7	2N3417	848A851H02	D9	1N645A	837A692H03
Q8	2N3417	848A851H02	D10	T1-55	183A790H09
Q9	2N3645	849A441H01	D11	T1-55	183A790H09
Q10	2N3417	848A851H02	D12	T1-55	183A790H09
Q11	2N3417	848A851H02	D13	T1-55	183A790H09
Q12	2N3417	848A851H02	D14	1N645A	837A692H03
Q13	2N3417	848A851H02	D15	T1-55	183A790H09
Q14	2N3645	849A441H01	D16	T1-55	183A790H09
Q15	2N3417	848A851H02	MISCELLANEOUS		
Q16	2N3417	848A851H02	CR	Close Relay	541D231H22
Q17	2N3417	848A851H02	IND. 1	Lockout Indicator	862A634G01

TYPE SGR-52 RECLOSING RELAY

CIRCUIT	TEST POINT	NORMAL INDICATIONS		COMPONENTS CHECKED
		BEFORE RESET	AFTER RESET	
Circuit Board Supply Voltage	Reset board terminal 1 Reclose board ter. 4 Indicator board ter. 2	20 V \pm 1.0 V	20 V \pm 1.0 V	Z4, R62
RESET CIRCUIT BOARD				
Timing Circuit	Junction of R10 and C2	Slow Voltage Rise to approx. 15 volts	Approx. 1.4 V	Q2, Q3, C2, C3, C13, R62, D3, D4
Flip-Flop Control	Junction of R14 and R18	Approx. 1 V	Approx. 15 V	Q4, Q5, C4
	Junction of R16 and R21	Approx. 15 V	Approx. 1 V	
Close Relay Circuit	Junction of R26 and D6	Approx. 0 V	Approx. 8 V	Q7
	Junction of Q9 and D7	Approx. 0 V	Approx. 20 V *	Q8, Q9, D7, D8

* With lead to Reclose Board Terminal 8 disconnected.

CIRCUIT	TEST POINT	NORMAL INDICATIONS		COMPONENTS CHECKED
		BEFORE PERMIT *	AFTER PERMIT *	
RECLOSE CIRCUIT BOARD				
Reclose Time Delay Circuit	Junction of D13 and R44	Approx. 0 V	Approx. 15 V	Q12, Q13, D13
	Junction of C11 and Z10	Approx. .5 V	Slow Voltage Rise to Approx. 6.8 or 9.1 Volts	Z10, D13, C11, R53, R63
	Junction of D16 and R61	Approx. 0 V	Time Delayed Approx. 20 V	Q16, Q17, D16
	Junction of Z7 and R46	BEFORE BLOCK * Approx. 0 V	AFTER BLOCK * Approx. 10 V Pulse	Q15, Q14, Z7, D12, D17

* + 20-volt d-c Permit signal applied to Relay terminal 5; 20-volt d-c Block signal applied to Relay terminal 6.

All measurements made between indicated points and d-c negative.

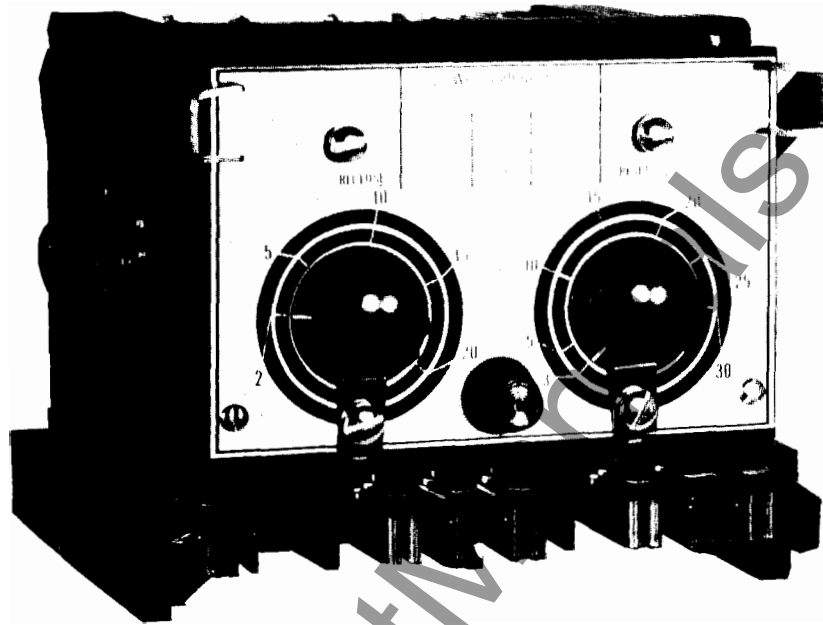


Fig. 1. Type SGR-52 Reclosing Relay (Front View).

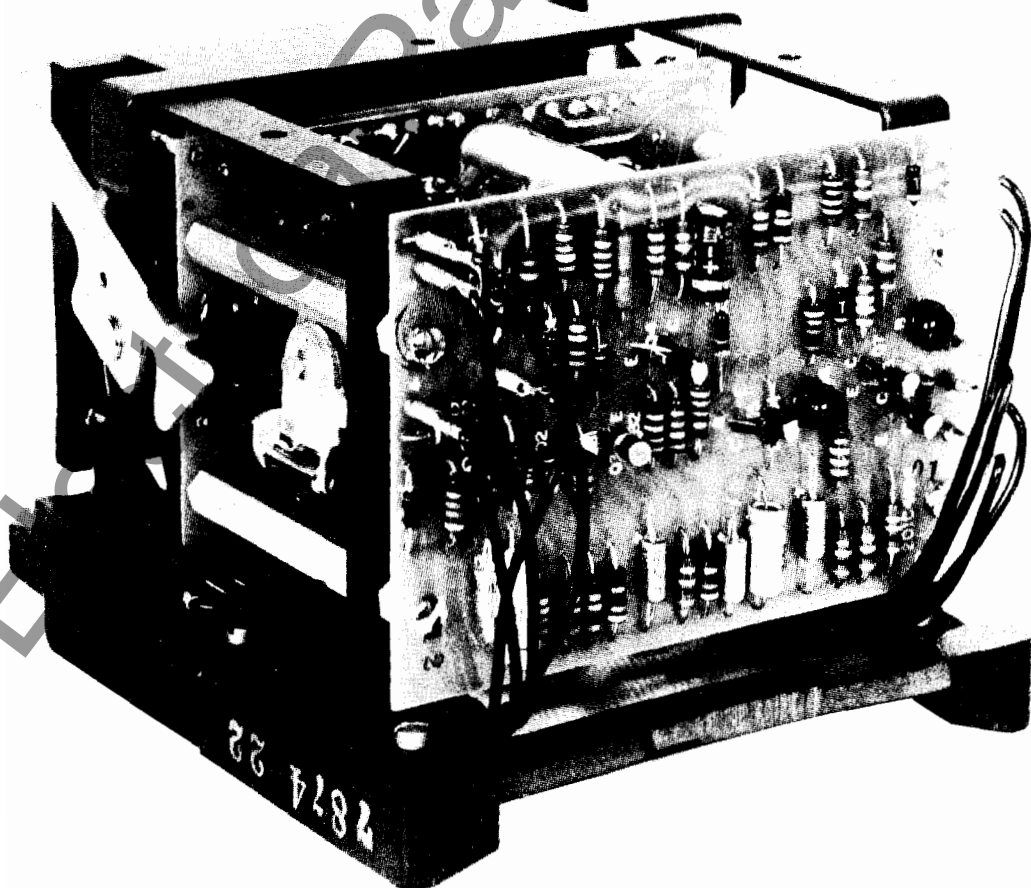


Fig. 2. Type SGR-52 Reclosing Relay (Rear View).

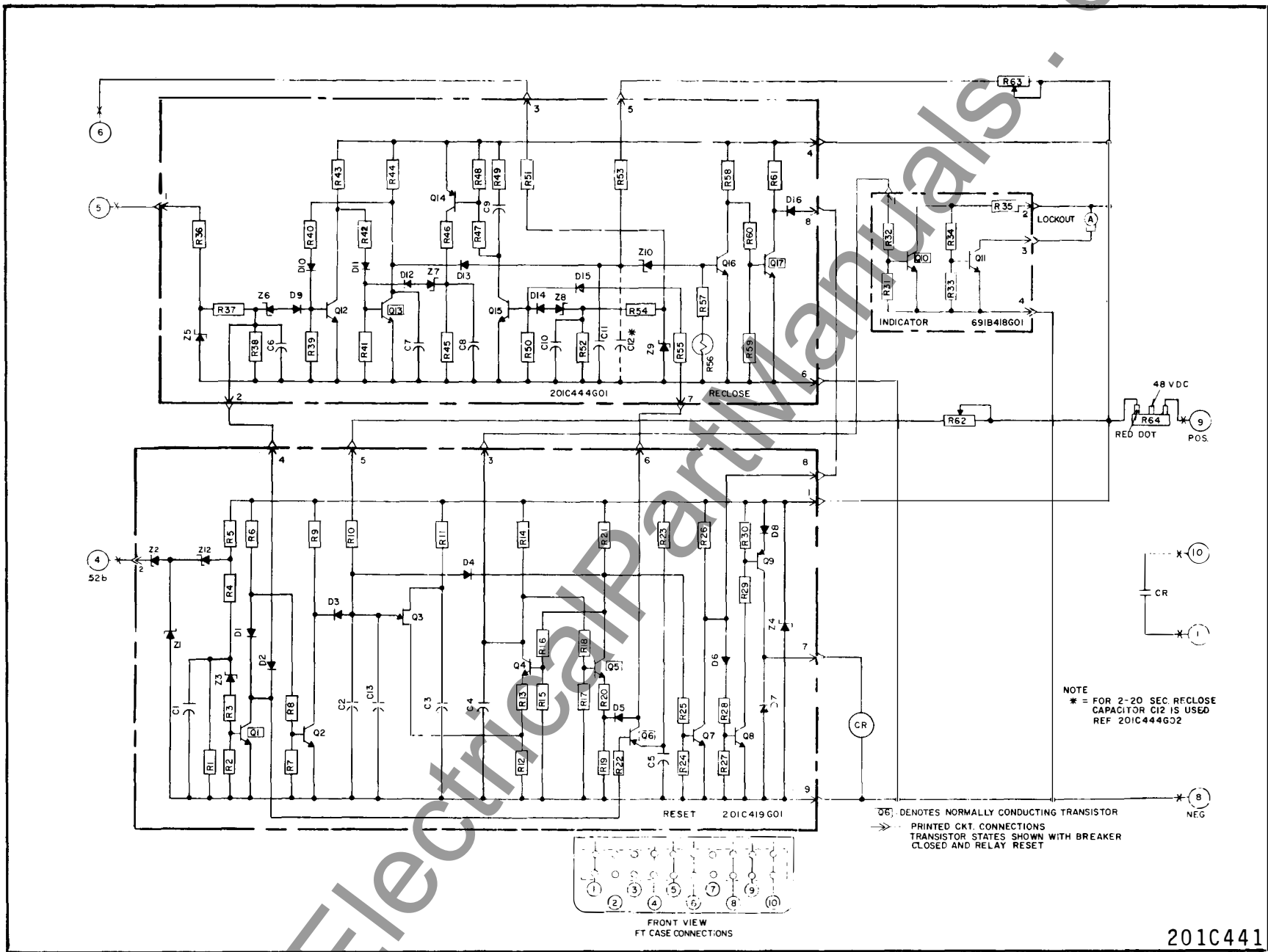


Fig. 3. Internal Schematic of Type SGR-52 Relay.

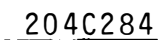


Fig. 4. Internal Schematic of Type SGR-52 Relay.

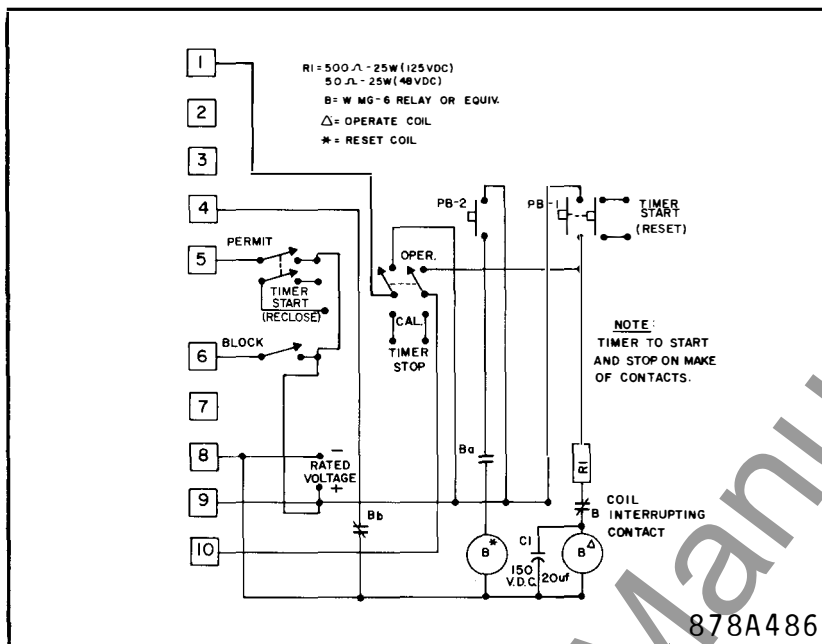


Fig. 5. Test Circuit of Type SGR-52 Relay.

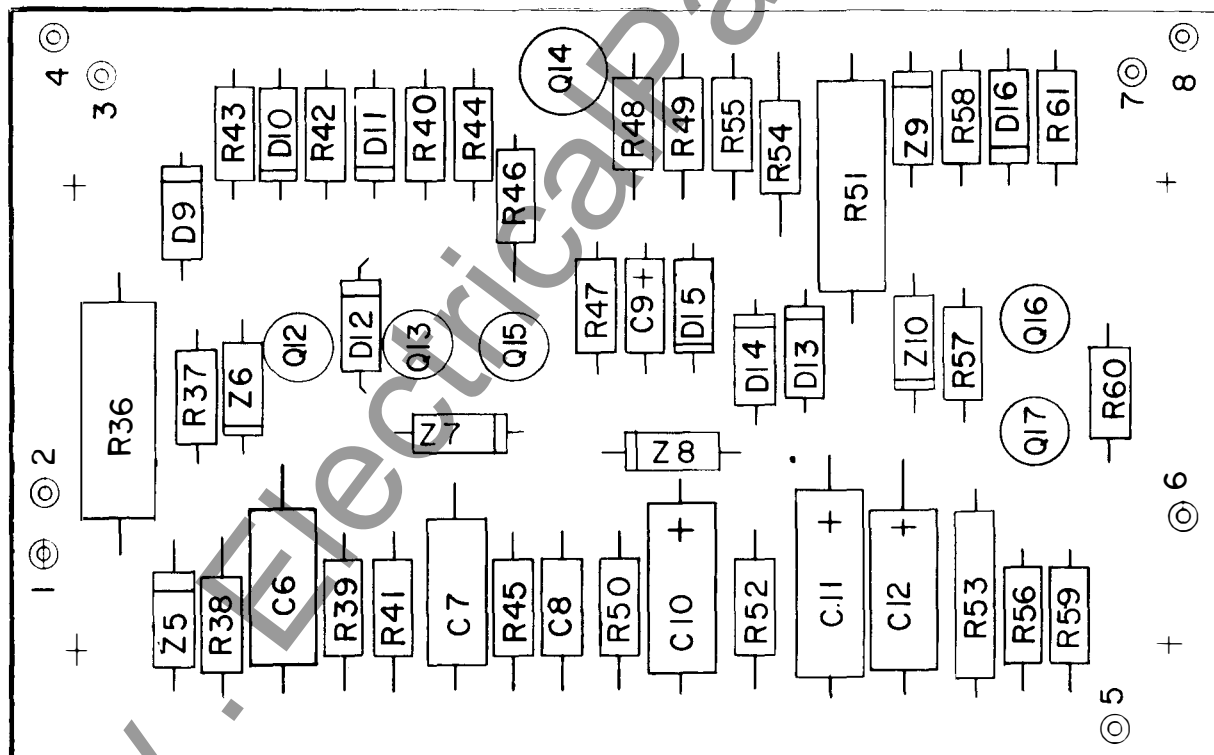


Fig. 6. Component Location of Reclose Printed Circuit Board for Type SGR-52 Relay.

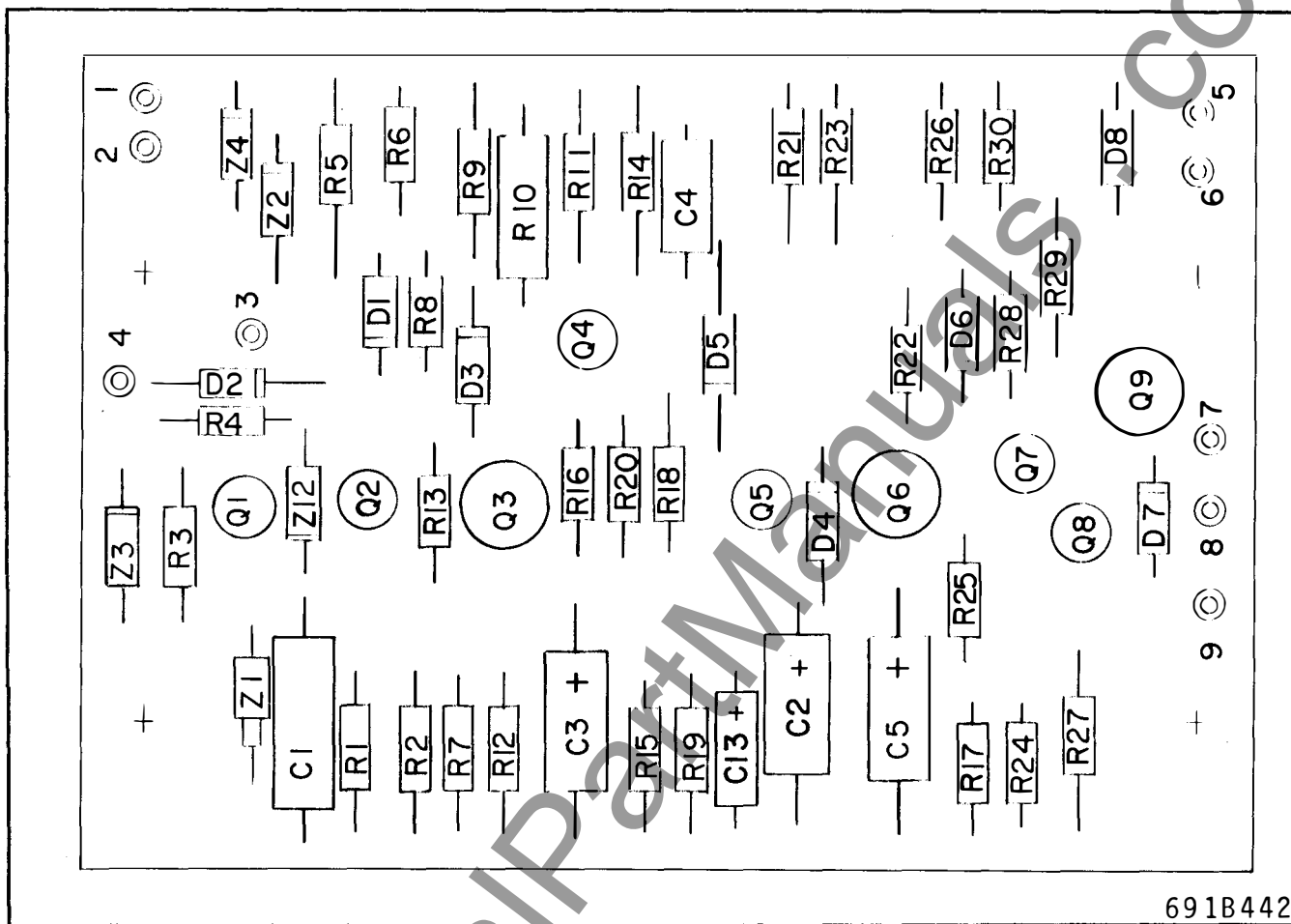


Fig. 7. Component Location of Reset Printed Circuit Board for Type SGR-52 Relay.

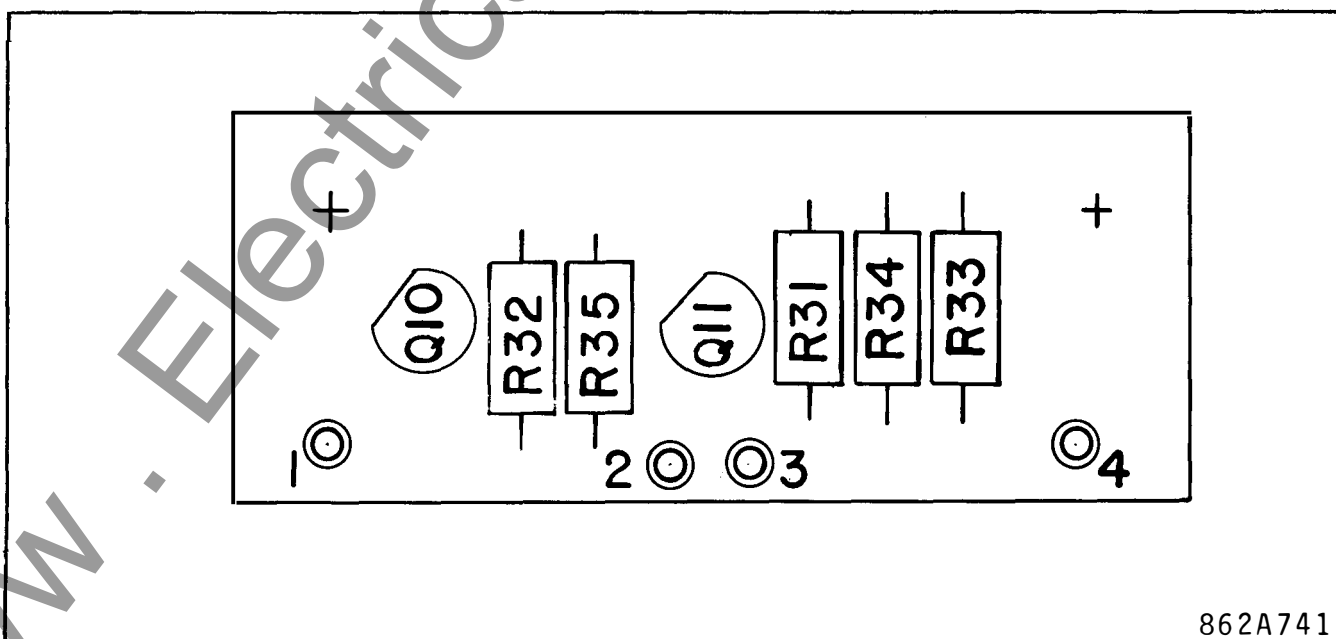


Fig. 8. Component Layout SGR-52 Indicator Board.



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